

AMENDMENTS TO THE SPECIFICATION

Please replace the following paragraphs as indicated below.

The paragraph beginning on page 1, line 4:

The present invention concerns methods for reducing detrimental phenomena related to disturb voltages in a data storage apparatus employing passive matrix addressing according to the introduction of claim 1, wherein the data storage cells of the data storage apparatus are provided in two or more electrically separated segments, each segment comprising a separate physical address space of the data storage apparatus.

The paragraph beginning on page 7, line 3:

It is often desirable, or even required, to internally divide the passive matrix structure into smaller "sub-matrices" or "segments", for instance to reduce power requirements or to reduce the number of disturbed cells during addressing. Segmentation may be accomplished in various ways, and for example instance be partial as disclosed in the present applicant's international published application W02/25665, which also discloses further prior art on segmentation of passive matrices in a preferred embodiment teaches the use of electrically segmented word lines in passive matrix-addressable ferroelectric memories.

The paragraph beginning on page 7, line 18:

- An addressing operation on a cell in ~~one a~~ segment ~~only shall give arise to~~ shall only cause substantial disturb voltages in the same segment.